

PIN	LABEL	SIGNAL	TO	FUNCTION	PIN	LABEL	SIGNAL	TO	FUNCTION	PIN	LABEL	SIGNAL	TO	FUNCTION			
1	GND	GND	TBB-1	GND TO GND TERMINALS	36	INP6	INP6	-	-	71	HOMEZ	HOMEZ	J13-19	FIDUCIAL	IMAGE FLAT		
2	ACMDX	ACMDX			37	INP5	INP5	-	-	72	FLSW	FLSW	J14-15	FWD LIMIT	LOW DISP. FLAT		
3	AENX	AENX			38	INP4/LW	INP4/LW	J14-5	LOW DISP. FLAT	73	RLSW	RLSW	J14-16	REV LIMIT		LOW DISP. FLAT	
4	PULSX	PULSX			39	INP3/LZ	INP3/LZ	J13-5	IMAGE FLAT	74	HOMEW	HOMEW	J14-19	FIDUCIAL			GND TO GND TERMINALS
5	DIRX	DIRX			40	INP2/LY	INP2/LY	J12-5	SPARE	75	GND	GND					
6	ACMDY	ACMDY			41	INP1/LX	INP1/LX	J11-5	POST DISP. PRISM	76	ABORT	ABORT					
7	AENY	AENY			42	INCOM	INCOM		( JUMPERED TO +5V ON BOARD )	77	XA+	XA+	J11-7	A+	ENCODER POST DISP. PRISM		
8	PULSY	PULSY			43	GND	GND		GND TO GND TERMINALS	78	XA-	XA-	J11-8	A-		ENCODER POST DISP. PRISM	
9	DIRY	DIRY			44	WAB-	WAB-			79	XB+	XB+	J11-9	B+			ENCODER POST DISP. PRISM
10	ACMDZ	ACMDZ			45	WAB+	WAB+			80	XB-	XB-	J11-10	B-			
11	AENZ	AENZ			46	WAA-	WAA-			81	XI+	XI+					
12	PULSZ	PULSZ			47	WAA+	WAA+			82	XI-	XI-					
13	DIRZ	DIRZ			48	ZAB-	ZAB-			83	YA+	YA+	J12-7	A+	SPARE ENCODER		
14	ACMDW	ACMDW			49	ZAB+	ZAB+			84	YA-	YA-	J12-8	A-		SPARE ENCODER	
15	AENW	AENW			50	ZAA-	ZAA-			85	YB+	YB+	J12-9	B+			SPARE ENCODER
16	PULSW	PULSW			51	ZAA+	ZAA+			86	YB-	YB-	J12-10	B-			
17	DIRW	DIRW			52	YAB-	YAB-			87	YI+	YI+					
18	AN1	AN1	AiIC P1-9	ANALOG INPUT 1	53	YAB+	YAB+			88	YI-	YI-					
19	AN2	AN2	AiIC P1-13	ANALOG INPUT 2	54	YAA-	YAA-			89	ZA+	ZA+	J13-7	A+	ENCODER IMAGE FLAT		
20	AN3	AN3	AiIC P1-11	ANALOG INPUT 3	55	YAA+	YAA+			90	ZA-	ZA-	J13-8	A-		ENCODER IMAGE FLAT	
21	AN4	AN4	-	-	56	XAB-	XAB-			91	ZB+	ZB+	J13-9	B+			ENCODER IMAGE FLAT
22	AN5	AN5	-	-	57	XAB+	XAB+			92	ZB-	ZB-	J13-10	B-			
23	AN6	AN6	-	-	58	XAA-	XAA-			93	ZI+	ZI+					
24	AN7	AN7	-	-	59	XAA+	XAA+			94	ZI-	ZI-					
25	GND	GND	AiIC P1-5	GND TO TEMPERATURE BOARD	60	GND	GND		GND TO GND TERMINALS	95	WA+	WA+	J14-7	A+	ENCODER LOW DISP. FLAT		
26	OUT1	OUT1	J11-21	POST DISP. PRISM	61	5V	5V		( JUMPERED TO INCOM AND LSCOM ON BOARD )	96	WA-	WA-	J14-8	A-		ENCODER LOW DISP. FLAT	
27	OUT2	OUT2	J12-21	SPARE	62	LSCOM	LSCOM		( JUMPERED TO +5V ON BOARD )	97	WB+	WB+	J14-9	B+			ENCODER LOW DISP. FLAT
28	OUT3	OUT3	J13-21	IMAGE FLAT	63	FLSX	FLSX	J11-15	FWD LIMIT	98	WB-	WB-	J14-10	B-			
29	OUT4	OUT4	J14-21	LOW DISP. FLAT	64	RLSX	RLSX	J11-16	REV LIMIT	99	WI+	WI+					
30	OUT5	OUT5	AiIC P1-6	ANALOG INPUT CONTROL 0	65	HOMEX	HOMEX	J11-19	FIDUCIAL	100	WI-	WI-					
31	OUT6	OUT6	AiIC P1-4	ANALOG INPUT CONTROL 1	66	FLSY	FLSY	J12-15	FWD LIMIT	101	+12V	+12V					
32	OUT7	OUT7	AiIC P1-2	ANALOG INPUT CONTROL 2	67	RLSY	RLSY	J12-16	REV LIMIT	102	-12V	-12V					
33	OUT8	OUT8	-	-	68	HOMEY	HOMEY	J12-19	FIDUCIAL	103	5V	5V					
34	INP8	INP8	-	-	69	FLSZ	FLSZ	J13-15	FWD LIMIT	104	GND	GND			GND TO GND TERMINALS		
35	INP7	INP7	-	-	70	RLSZ	RLSZ	J13-16	REV LIMIT								

- NOTE: 1. THOUGH NOTED AS X, Y, Z, AND W-AXIS  
WE HAVE DESIGNATED THESE AS A, B, C,  
AND D-AXIS
2. NORMAL ENCODING ON PRISM & LAMP STAGES.

UNIVERSITY OF CALIFORNIA  
LICK OBSERVATORY

SIGNAL ASSIGNMENTS  
CONTROLLER #1, 1ST AMPLIFIER  
ESI SPECTROGRAPH

REVISION

11/19/97 ADDED NOTES AND CORRECTED TEMPERATURE SIGNALS  
1-15-98 CHANGED IMAGE AND LOW DISPERSION FLATS FROM AIR TO SERVO STAGES  
12-8-98 REMOVED LAMP STAGE REFERENCES

DES'N BY: B. Alcott

ORIGIN DATE: 2/19/96

DWG. NO.

NUM. 1 OF 1

DRAWN BY:

MODIFY DATE: 12-8-98

PATH: ESI\SIGNALS7

REV. A

EL-3326-1D