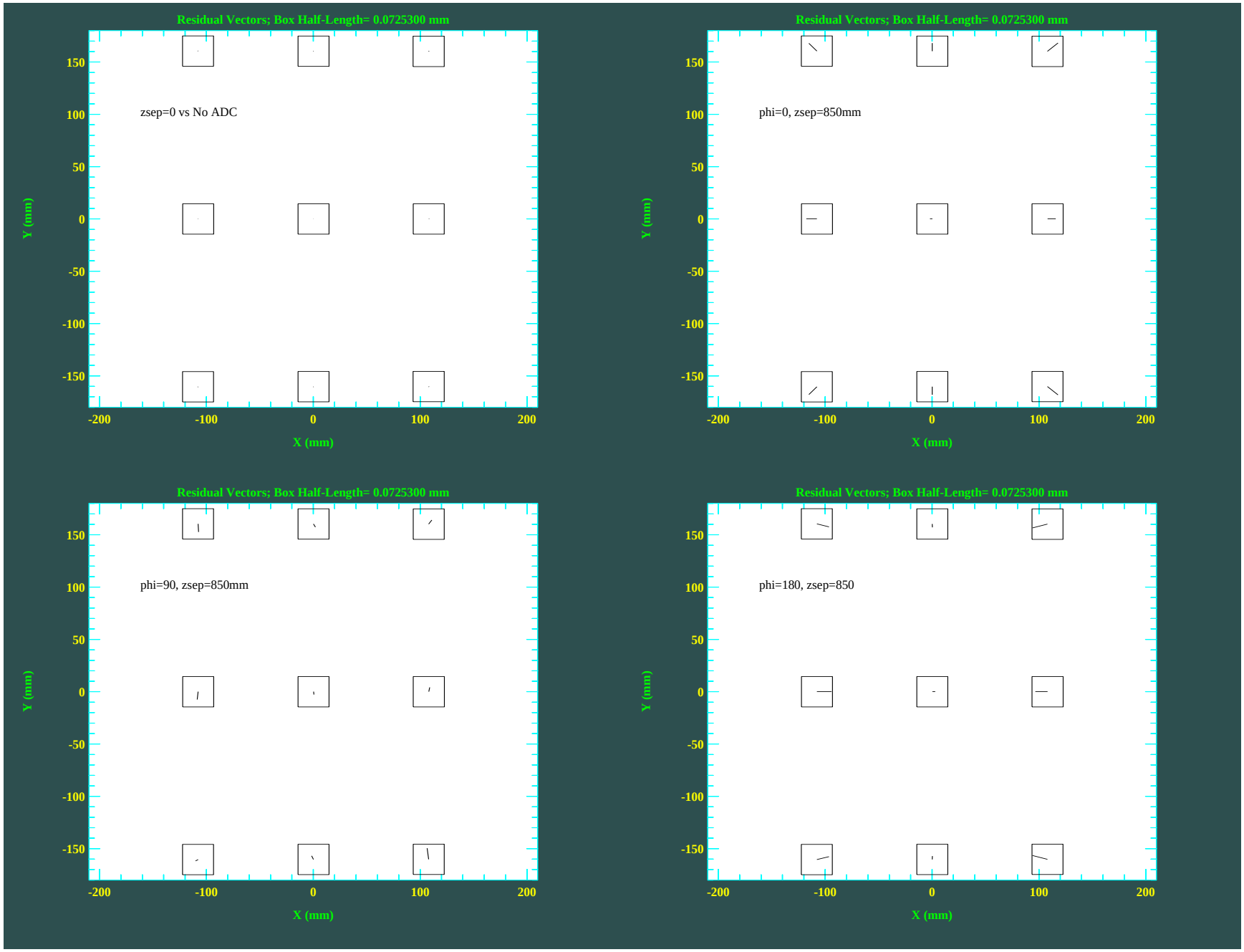Differential Distortion for LRIS ADC; each box is 0.2 arcsec on a side